

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

B. Sc. Hons Electronics I - Sem

w.e.f August 01, 2025

Time table for:

B. Sc. Hons Electronics I - Sem

W.R.T August 01, 2023

|           | 1<br>9 am-10 am         | 2<br>10 am-11 am            | 3<br>11 am-12 pm             | 4<br>12 pm-1 pm              | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm                        | 6<br>2:30pm -3:30 pm                        | 7<br>3:30 pm-4:30 pm | 8<br>4:30 pm-5:30 pm |  |
|-----------|-------------------------|-----------------------------|------------------------------|------------------------------|--------------|---------------------------------------------|---------------------------------------------|----------------------|----------------------|--|
| Monday    |                         |                             | Prog. Python<br>NK<br>117    | Prog. Python<br>NK<br>117    | LUNCH BREAK  | Circuit. Theory Lab<br>AS, N, NT<br>117     | Circuit. Theory Lab<br>AS, N, NT<br>117     | VAC - Sem I          | VAC - Sem I          |  |
| Tuesday   |                         | Crcuit. Theory<br>AS<br>116 |                              | Semi. Dev.<br>NT<br>116      |              | Semi. Dev. Lab<br>NT, AS, RK<br>117         | Semi. Dev. Lab<br>NT, AS, RK<br>117         | SEC<br>AS<br>116     | SEC<br>AS<br>116     |  |
| Wednesday |                         |                             |                              | Prog. Python<br>NK<br>104    |              | Prog. Python Lab.<br>NK, NT, RK<br>116, 117 | Prog. Python Lab.<br>NK, NT, RK<br>116, 117 |                      |                      |  |
| Thursday  | Generic Lab./Tutorial   |                             |                              |                              |              |                                             |                                             | SEC<br>AS<br>116     | SEC<br>AS<br>116     |  |
| Friday    | Semi. Dev.<br>NT<br>116 | Semi. Dev.<br>NT<br>116     | Circuit. Theory<br>AS<br>116 | Circuit. Theory<br>AS<br>116 |              | AEC-Sem-I                                   |                                             |                      |                      |  |
| Saturday  | Generic                 | Generic                     | VAC - Sem I                  | VAC - Sem I                  |              |                                             |                                             |                      |                      |  |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

| Abbr.           | Title of the paper                    | Type | DU Paper Code |
|-----------------|---------------------------------------|------|---------------|
| Prog. Python    | Programming Fundamentals using Python | Core | 2512011101    |
| Circuit. Theory | Circuit Theory and Network Analysis   | Core | 2512011102    |
| Semi. Dev.      | Semiconductor Devices                 | Core | 2512011103    |

RK Prof. Ravinder Kaur  
PKS Prof. Poonam Kasturi  
NT Prof. Neeraj Tyagi  
AM Prof. Anurag Mishra  
MS Prof. Manoj Saxena

N Neha  
NK Naveen Kumar  
AS Ajit Singh  
G Guest

Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

B. Sc. Hons Electronics III - Sem

w.e.f August 01, 2025

| Time table for: |                                  |                                  |                                  |                                  | B. Sc. Hons Electronics III - Sem |                                    |                                    |                                |                      | w.e.f August 01, 2025 |  |  |  |  |
|-----------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|-----------------------------------|------------------------------------|------------------------------------|--------------------------------|----------------------|-----------------------|--|--|--|--|
|                 | 1<br>9 am-10 am                  | 2<br>10 am-11 am                 | 3<br>11 am-12 pm                 | 4<br>12 pm-1 pm                  | 1 pm-1:30 pm                      | 5<br>1:30 pm-2:30 pm               | 6<br>2:30 pm-3:30 pm               | 7<br>3:30 pm-4:30 pm           | 8<br>4:30 pm-5:30 pm |                       |  |  |  |  |
| Monday          | Signal Sys<br>NK<br>115          | Signal Sys<br>NK<br>115          | Anal Elect-II<br>RK<br>104       | Anal Elect-II<br>RK<br>104       | LUNCH BREAK                       | AEC-Sem-III                        |                                    |                                |                      |                       |  |  |  |  |
| Tuesday         | Math.Found.Lab-Bt-1<br>MS<br>117 | Math.Found.Lab-Bt-1<br>MS<br>117 | Math.Found.Lab-Bt-2<br>MS<br>117 | Math.Found.Lab-Bt-2<br>MS<br>117 |                                   | Engg. Math<br>N<br>104             | Engg. Math Lab.<br>N, G<br>117     | Engg. Math Lab.<br>N, G<br>117 |                      |                       |  |  |  |  |
| Wednesday       | SEC<br>PKS<br>117                |                                  |                                  |                                  |                                   | Engg. Math<br>N<br>104             | Engg. Math<br>N<br>104             |                                |                      |                       |  |  |  |  |
| Thursday        |                                  | Math. Found.<br>MS<br>116        |                                  |                                  |                                   | Anal Elect-II Lab.<br>NK, G<br>117 | Anal Elect-II Lab.<br>NK, G<br>117 |                                |                      |                       |  |  |  |  |
| Friday          | Math. Found.<br>MS<br>115        | Math. Found.<br>MS<br>115        | Signal Sys<br>NK<br>115          | Anal Elect-II<br>RK<br>115       |                                   | Signal Sys Lab.<br>NK, RK<br>116   | Signal Sys Lab.<br>NK, RK<br>116   | VAC - Sem III                  | VAC - Sem III        |                       |  |  |  |  |
| Saturday        | VAC - Sem III                    | VAC - Sem III                    |                                  |                                  |                                   |                                    |                                    |                                |                      |                       |  |  |  |  |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

| Abbr.         | Title of the paper                   | Type | DU Paper Code |
|---------------|--------------------------------------|------|---------------|
| Engg. Math    | Engineering Mathematics              | Core | 2512012301    |
| Anal Elect-II | Analog Electronics-II                | Core | 2512012302    |
| Signal Sys    | Signals and Systems                  | Core | 2512012303    |
| Math. Found.  | Mathematics Foundation for Computing | DSE  | 2513012003    |

RK Prof. Ravinder Kaur  
PKS Prof. Poonam Kasturi  
NT Prof. Neeraj Tyagi  
AM Prof. Anurag Mishra  
MS Prof. Manoj Saxena

N Neha  
NK Naveen Kumar  
AS Ajit Singh

Convener Time Table

Principal

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**B. Sc. Hons Electronics V - Sem**

**w.e.f August 01, 2025**

|                  | 1<br>9 am-10 am                | 2<br>10 am-11 am               | 3<br>11 am-12 pm                 | 4<br>12 pm-1 pm                  | 1 pm-1:30 pm       | 5<br>1:30 pm-2:30 pm      | 6<br>2:30pm-3:30 pm       | 7<br>3:30 pm-4:30 pm           | 8<br>4:30 pm-5:30 pm           |
|------------------|--------------------------------|--------------------------------|----------------------------------|----------------------------------|--------------------|---------------------------|---------------------------|--------------------------------|--------------------------------|
| <b>Monday</b>    | Generic                        | Generic                        | Embedded Sys.<br>PKS<br>116      | Embedded Sys.<br>PKS<br>116      | <b>LUNCH BREAK</b> | VLSI<br>MS<br>115         | Comp. N/W<br>AM<br>115    | VLSI Lab. Bt-2<br>MS<br>116    | VLSI Lab. Bt-2<br>MS<br>115    |
| <b>Tuesday</b>   |                                |                                |                                  | EMT<br>AS<br>104                 |                    | Comp. N/W<br>AM<br>115    | Comp. N/W<br>AM<br>115    |                                |                                |
| <b>Wednesday</b> | VLSI Lab. Bt-1<br>MS<br>116    | VLSI Lab. Bt-1<br>MS<br>116    | VLSI Theory<br>MS<br>116         | VLSI Theory<br>MS<br>116         |                    | Generic Lab./Tutorial     |                           |                                |                                |
| <b>Thursday</b>  | Embedded Sys.<br>PKS<br>115    |                                | EMT<br>AS<br>116                 | EMT<br>AS<br>116                 |                    | EMT Lab.<br>AS, NT<br>116 | EMT Lab.<br>AS, NT<br>116 | Comp. N/W Lab.<br>AM, N<br>117 | Comp. N/W Lab.<br>AM, N<br>117 |
| <b>Friday</b>    | Embedded Lab. Bt-1<br>G<br>117 | Embedded Lab. Bt-1<br>G<br>117 | Embedded Lab. Bt-2<br>PKS<br>117 | Embedded Lab. Bt-2<br>PKS<br>117 |                    | SEC<br>AM<br>117          | SEC<br>AM<br>117          | SEC<br>AM<br>117               | SEC<br>AM<br>117               |
| <b>Saturday</b>  |                                |                                |                                  |                                  |                    |                           |                           |                                |                                |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

| Abbr.         | Title of the paper | Type | DU Paper Code |
|---------------|--------------------|------|---------------|
| EMT           | Electromagnetics   | Core | 2512013502    |
| Embedded Sys. | Embedded System    | Core | 2512013501    |
| VLSI          | Basic VLSI Design  | Core | 2512013503    |
| Comp. N/W     | Computer Networks  | DSE  | 2512010007    |

|     |                      |    |              |
|-----|----------------------|----|--------------|
| RK  | Prof. Ravinder Kaur  | N  | Neha         |
| PKS | Prof. Poonam Kasturi | NK | Naveen Kumar |
| NT  | Prof. Neeraj Tyagi   | AS | Ajit Singh   |
| AM  | Prof. Anurag Mishra  | G  | Guest        |
| MS  | Prof. Manoj Saxena   |    |              |

Convener

Time Table

Principal

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**B. Sc. Hons Electronics VII - Sem**

**w.e.f August 01, 2025**

| Time Table for: B. Sc. Hons Electronics VII - Sem |                                    |                         |                          |                          |              | W.R.T August 01, 2023              |                               |                              |                              |  |
|---------------------------------------------------|------------------------------------|-------------------------|--------------------------|--------------------------|--------------|------------------------------------|-------------------------------|------------------------------|------------------------------|--|
|                                                   | 1<br>9 am-10 am                    | 2<br>10 am-11 am        | 3<br>11 am-12 pm         | 4<br>12 pm-1 pm          | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm               | 6<br>2:30pm-3:30 pm           | 7<br>3:30 pm-4:30 pm         | 8<br>4:30 pm-5:30 pm         |  |
| Monday                                            |                                    |                         | Nanoscience<br>NT<br>115 | Nanoscience<br>NT<br>115 | LUNCH BREAK  | Control Sys. Lab<br>RK<br>116      | Control Sys. Lab<br>RK<br>116 | Neural N/w<br>AM<br>115      |                              |  |
| Tuesday                                           |                                    | CMOS VLSI<br>PKS<br>115 | CMOS VLSI<br>PKS<br>115  | Control Sys<br>RK<br>115 |              | CMOS VLSI Lab.<br>PKS<br>116       | CMOS VLSI Lab.<br>PKS<br>116  |                              |                              |  |
| Wednesday                                         |                                    |                         | Nanoscience<br>NT<br>115 | Nanoscience<br>NT<br>115 |              | Neural N/w<br>AM<br>115            | Neural N/w<br>AM<br>115       | Neural N/w Lab.<br>AM<br>117 | Neural N/w Lab.<br>AM<br>117 |  |
| Thursday                                          |                                    | CMOS VLSI<br>PKS<br>115 | Control Sys<br>RK<br>115 | Control Sys<br>RK<br>115 |              | Dissertation/Academic Project Work |                               |                              |                              |  |
| Friday                                            | Dissertation/Academic Project Work |                         |                          |                          |              | Dissertation/Academic Project Work |                               |                              |                              |  |
| Saturday                                          |                                    |                         |                          |                          |              |                                    |                               |                              |                              |  |

Classes begin at 9:00 am on all days.

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| Abbr.       | Title of the paper                | Type | DU Paper Code |
|-------------|-----------------------------------|------|---------------|
| Control Sys | Control Systems                   | Core |               |
| Neural N/w  | Neural Networks and Deep Learning | DSE  |               |
| CMOS VLSI   | CMOS Digital VLSI Design          | DSE  |               |
| Nanoscience | Introduction to Nanoscience       | DSE  |               |

|     |                      |    |              |
|-----|----------------------|----|--------------|
| RK  | Prof. Ravinder Kaur  | N  | Neha         |
| PKS | Prof. Poonam Kasturi | NK | Naveen Kumar |
| NT  | Prof. Neeraj Tyagi   | AS | Ajit Singh   |
| AM  | Prof. Anurag Mishra  |    |              |
| MS  | Prof. Manoj Saxena   |    |              |

Convener

Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

w.e.f August 01, 2025

Time table for:

Prof. Ravinder Kaur

|           | 1<br>9 am-10 am | 2<br>10 am-11 am | 3<br>11 am-12 pm               | 4<br>12 pm-1 pm                | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm                  | 6<br>2:30 pm-3:30 pm                  | 7<br>3:30 pm-4:30 pm | 8<br>4:30 pm-5:30 pm |
|-----------|-----------------|------------------|--------------------------------|--------------------------------|--------------|---------------------------------------|---------------------------------------|----------------------|----------------------|
| Monday    |                 |                  | Anal Elect-II<br>EH-III<br>104 | Anal Elect-II<br>EH-III<br>104 | LUNCH BREAK  | Control Sys. Lab<br>EH-VII<br>116     | Control Sys. Lab<br>EH-VII<br>116     |                      |                      |
| Tuesday   |                 |                  |                                | Control Sys<br>EH-VII<br>115   |              | Seml. Dev. Lab<br>EH-I<br>117         | Seml. Dev. Lab<br>EH-I<br>117         |                      |                      |
| Wednesday |                 |                  |                                |                                |              | Prog. Python Lab.<br>EH-I<br>116, 117 | Prog. Python Lab.<br>EH-I<br>116, 117 |                      |                      |
| Thursday  |                 |                  | Control Sys<br>115<br>EH-VII   | Control Sys<br>115<br>EH-VII   |              |                                       |                                       |                      |                      |
| Friday    |                 |                  |                                | Anal Elect-II<br>EH-III<br>115 |              | Signal Sys Lab.<br>EH-II<br>116       | Signal Sys Lab.<br>EH-II<br>116       |                      |                      |
| Saturday  |                 |                  |                                |                                |              |                                       |                                       |                      |                      |

Classes begin at 9:00 am on all days.

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DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

w.e.f August 07, 2025

Time table for:

Prof. Poonam Kasturi

|           | 1<br>9 am-10 am              | 2<br>10 am-11 am           | 3<br>11 am-12 pm                  | 4<br>12 pm-1 pm                   | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm            | 6<br>2:30 pm-3:30 pm            | 7<br>3:30 pm-4:30 pm | 8<br>4:30 pm-5:30 pm |
|-----------|------------------------------|----------------------------|-----------------------------------|-----------------------------------|--------------|---------------------------------|---------------------------------|----------------------|----------------------|
| Monday    |                              |                            | Embedded Sys.<br>116<br>EH-V      | Embedded Sys.<br>116<br>EH-V      | LUNCH BREAK  |                                 |                                 |                      |                      |
| Tuesday   |                              | CMOS VLSI<br>EH-VII<br>115 | CMOS VLSI<br>EH-VII<br>115        |                                   |              | CMOS VLSI Lab.<br>EH-VII<br>116 | CMOS VLSI Lab.<br>EH-VII<br>116 |                      |                      |
| Wednesday |                              | SEC<br>EH-III<br>117       |                                   |                                   |              |                                 |                                 |                      |                      |
| Thursday  | Embedded Sys.<br>EH-V<br>115 | CMOS VLSI<br>PKS<br>115    |                                   |                                   |              |                                 |                                 |                      |                      |
| Friday    |                              |                            | Embedded Lab. Bt-2<br>EH-V<br>117 | Embedded Lab. Bt-2<br>EH-V<br>117 |              |                                 |                                 |                      |                      |
| Saturday  |                              |                            |                                   |                                   |              |                                 |                                 |                      |                      |

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal



DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

Prof. Anurag Mishra

w.e.f August 01, 2025

|           | 1<br>9 am-10 am | 2<br>10 am-11 am | 3<br>11 am-12 pm | 4<br>12 pm-1 pm | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm        | 6<br>2:30pm-3:30 pm         | 7<br>3:30 pm-4:30 pm             | 8<br>4:30 pm-5:30 pm             |
|-----------|-----------------|------------------|------------------|-----------------|--------------|-----------------------------|-----------------------------|----------------------------------|----------------------------------|
| Monday    |                 |                  |                  |                 | LUNCH BREAK  |                             | Comp. N/W<br>EH-V<br>115    | Neural N/w<br>EH-VII<br>115      |                                  |
| Tuesday   |                 |                  |                  |                 |              | Comp. N/W<br>EH-V<br>115    | Comp. N/W<br>EH-V<br>115    |                                  |                                  |
| Wednesday |                 |                  |                  |                 |              | Neural N/w<br>EH-VII<br>115 | Neural N/w<br>EH-VII<br>115 | Neural N/w Lab.<br>EH-VII<br>117 | Neural N/w Lab.<br>EH-VII<br>117 |
| Thursday  |                 |                  |                  |                 |              |                             |                             | Comp. N/W Lab.<br>EH-V<br>117    | Comp. N/W Lab.<br>EH-V<br>117    |
| Friday    |                 |                  |                  |                 |              | SEC<br>EH-V<br>117          | SEC<br>EH-V<br>117          | SEC<br>EH-V<br>117               | SEC<br>EH-V<br>117               |
| Saturday  |                 |                  |                  |                 |              |                             |                             |                                  |                                  |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Signature  
Convener Time Table

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Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

Prof. Neeraj Tyagi

w.e.f August 01, 2025

|           | 1<br>9 am-10 am           | 2<br>10 am-11 am          | 3<br>11 am-12 pm             | 4<br>12 pm-1 pm              | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm                  | 6<br>2:30pm-3:30 pm                   | 7<br>3:30 pm-4:30 pm | 8<br>4:30 pm-5:30 pm |
|-----------|---------------------------|---------------------------|------------------------------|------------------------------|--------------|---------------------------------------|---------------------------------------|----------------------|----------------------|
| Monday    |                           |                           | Nanoscience<br>EH-VII<br>115 | Nanoscience<br>EH-VII<br>115 | LUNCH BREAK  | Circuit. Theory Lab<br>EH-I<br>117    | Circuit. Theory Lab<br>EH-I<br>117    |                      |                      |
| Tuesday   |                           |                           |                              | Semi. Dev.<br>EH-I<br>116    |              | Semi. Dev. Lab<br>EH-I<br>117         | Semi. Dev. Lab<br>EH-I<br>117         |                      |                      |
| Wednesday |                           |                           | Nanoscience<br>NT<br>115     | Nanoscience<br>NT<br>115     |              | Prog. Python Lab.<br>EH-I<br>116, 117 | Prog. Python Lab.<br>EH-I<br>116, 117 |                      |                      |
| Thursday  |                           |                           |                              |                              |              | EMT Lab.<br>EH-V<br>116               | EMT Lab.<br>EH-V<br>116               |                      |                      |
| Friday    | Semi. Dev.<br>EH-I<br>116 | Semi. Dev.<br>EH-I<br>116 |                              |                              |              |                                       |                                       |                      |                      |
| Saturday  |                           |                           |                              |                              |              |                                       |                                       |                      |                      |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Signature  
Convener Time Table

Signature  
Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

Prof. Manoj Saxena

w.e.f August 01, 2025

|           | 1<br>9 am-10 am                      | 2<br>10 am-11 am                     | 3<br>11 am-12 pm                     | 4<br>12 pm-1 pm                      | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm | 6<br>2:30pm-3:30 pm | 7<br>3:30 pm-4:30 pm          | 8<br>4:30 pm-5:30 pm          |
|-----------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------|----------------------|---------------------|-------------------------------|-------------------------------|
| Monday    |                                      |                                      |                                      |                                      | LUNCH BREAK  | VLSI<br>EH-V<br>115  |                     | VLSI Lab. Bt-2<br>EH-V<br>116 | VLSI Lab. Bt-2<br>EH-V<br>116 |
| Tuesday   | Math.Found.Lab-Bt-1<br>EH-III<br>117 | Math.Found.Lab-Bt-1<br>EH-III<br>117 | Math.Found.Lab-Bt-2<br>EH-III<br>117 | Math.Found.Lab-Bt-2<br>EH-III<br>117 |              |                      |                     |                               |                               |
| Wednesday | VLSI Lab. Bt-1<br>EH-V<br>116        | VLSI Lab. Bt-1<br>EH-V<br>116        | VLSI Theory<br>EH-V<br>116           | VLSI Theory<br>EH-V<br>116           |              |                      |                     |                               |                               |
| Thursday  |                                      | Math. Found.<br>MS<br>116            |                                      |                                      |              |                      |                     |                               |                               |
| Friday    | Math. Found.<br>EH-III<br>115        | Math. Found.<br>EH-III<br>115        |                                      |                                      |              |                      |                     |                               |                               |
| Saturday  |                                      |                                      |                                      |                                      |              |                      |                     |                               |                               |

Classes begin at 9:00 am on all days.

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DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

NEHA

w.e.f August 01, 2025

|           | 1<br>9 am-10 am                      | 2<br>10 am-11 am     | 3<br>11 am-12 pm | 4<br>12 pm-1 pm | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm               | 6<br>2:30pm-3:30 pm                | 7<br>3:30 pm-4:30 pm             | 8<br>4:30 pm-5:30 pm             |
|-----------|--------------------------------------|----------------------|------------------|-----------------|--------------|------------------------------------|------------------------------------|----------------------------------|----------------------------------|
| Monday    | Generic Sem V<br>104                 | Generic Sem V<br>104 |                  |                 | LUNCH BREAK  | Circuit. Theory Lab<br>EH-I<br>117 | Circuit. Theory Lab<br>EH-I<br>117 |                                  |                                  |
| Tuesday   |                                      |                      |                  |                 |              | Engg. Math<br>EH-III<br>104        |                                    | Engg. Math Lab.<br>EH-III<br>117 | Engg. Math Lab.<br>EH-III<br>117 |
| Wednesday |                                      |                      |                  |                 |              |                                    | Engg. Math<br>EH-III<br>104        | Engg. Math<br>EH-III<br>104      |                                  |
| Thursday  | Generic Lab. + 1 Theory Sem-I<br>117 |                      |                  |                 |              |                                    |                                    | Comp. N/W Lab.<br>EH-V<br>117    | Comp. N/W Lab.<br>EH-V<br>117    |
| Friday    |                                      |                      |                  |                 |              |                                    |                                    |                                  |                                  |
| Saturday  | Generic Sem I<br>115                 | Generic Sem I<br>115 |                  |                 |              |                                    |                                    |                                  |                                  |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

Naveen Kumar

w.e.f August 01, 2025

|           | 1<br>9 am-10 am             | 2<br>10 am-11 am            | 3<br>11 am-12 pm            | 4<br>12 pm-1 pm             | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm                  | 6<br>2:30pm -3:30 pm                  | 7<br>3:30 pm-4:30 pm         | 8<br>4:30 pm-5:30 pm         |
|-----------|-----------------------------|-----------------------------|-----------------------------|-----------------------------|--------------|---------------------------------------|---------------------------------------|------------------------------|------------------------------|
| Monday    | Signal Sys<br>EH-III<br>115 | Signal Sys<br>EH-III<br>115 | Prog. Python<br>EH-I<br>117 | Prog. Python<br>EH-I<br>117 | LUNCH BREAK  |                                       |                                       |                              |                              |
| Tuesday   |                             |                             |                             |                             |              |                                       |                                       |                              |                              |
| Wednesday |                             |                             |                             | Prog. Python<br>EH-I<br>104 |              | Prog. Python Lab.<br>EH-I<br>116, 117 | Prog. Python Lab.<br>EH-I<br>116, 117 | Generic Lab.<br>Sem V<br>116 | Generic Lab.<br>Sem V<br>116 |
| Thursday  |                             |                             |                             |                             |              | Anal Elect-II Lab.<br>EH-III<br>117   | Anal Elect-II Lab.<br>EH-III<br>117   |                              |                              |
| Friday    |                             |                             | Signal Sys<br>EH-III<br>115 |                             |              | Signal Sys Lab.<br>EH-III<br>116      | Signal Sys Lab.<br>EH-III<br>116      | VAC - Sem III<br>116         | VAC - Sem III<br>116         |
| Saturday  | VAC - Sem III<br>104        | VAC - Sem III<br>104        |                             |                             |              |                                       |                                       |                              |                              |

Classes begin at 9:00 am on all days.

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DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

Ajit

w.e.f August 02, 2025

|           | 1<br>9 am-10 am | 2<br>10 am-11 am              | 3<br>11 am-12 pm              | 4<br>12 pm-1 pm               | 1 pm-1:30 pm | 5<br>1:30 pm-2:30 pm              | 6<br>2:30pm -3:30 pm              | 7<br>3:30 pm-4:30 pm | 8<br>4:30 pm-5:30 pm |
|-----------|-----------------|-------------------------------|-------------------------------|-------------------------------|--------------|-----------------------------------|-----------------------------------|----------------------|----------------------|
| Monday    |                 |                               |                               |                               | LUNCH BREAK  | Circuit Theory Lab<br>EH-I<br>117 | Circuit Theory Lab<br>EH-I<br>117 |                      |                      |
| Tuesday   |                 | Circuit Theory<br>EH-I<br>116 |                               | EMT<br>AS<br>104              |              | Semi. Dev. Lab<br>EH-I<br>117     | Semi. Dev. Lab<br>EH-I<br>117     | SEC<br>EH-I<br>116   | SEC<br>EH-I<br>116   |
| Wednesday |                 |                               |                               |                               |              |                                   | Generic<br>Sem V<br>111           |                      |                      |
| Thursday  |                 |                               | EMT<br>EH-V<br>116            | EMT<br>EH-V<br>116            |              | EMT Lab.<br>EH-V<br>116           | EMT Lab.<br>EH-V<br>116           | SEC<br>EH-I<br>116   | SEC<br>EH-I<br>116   |
| Friday    |                 |                               | Circuit Theory<br>EH-I<br>116 | Circuit Theory<br>EH-I<br>116 |              |                                   |                                   |                      |                      |
| Saturday  |                 |                               |                               |                               |              |                                   |                                   |                      |                      |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

w.e.f August 01, 2025

Time table for:

Guest

| Time table for: | Guest                             |                                   |                           |                    | w.e.f August 01, 2025 |                                     |                                      |                                  |                                  |
|-----------------|-----------------------------------|-----------------------------------|---------------------------|--------------------|-----------------------|-------------------------------------|--------------------------------------|----------------------------------|----------------------------------|
|                 | 1<br>9 am-10 am                   | 2<br>10 am-11 am                  | 3<br>11 am-12 pm          | 4<br>12 pm-1 pm    | 1 pm-1:30 pm          | 5<br>1:30 pm-2:30 pm                | 6<br>2:30pm -3:30 pm                 | 7<br>3:30 pm-4:30 pm             | 8<br>4:30 pm-5:30 pm             |
| Monday          |                                   |                                   |                           |                    | LUNCH BREAK           |                                     |                                      | VAC - Sem I<br>104               | VAC - Sem I<br>104               |
| Tuesday         |                                   |                                   |                           |                    |                       |                                     |                                      | Engg. Math Lab.<br>EH-III<br>117 | Engg. Math Lab.<br>EH-III<br>117 |
| Wednesday       |                                   |                                   |                           |                    |                       |                                     |                                      |                                  |                                  |
| Thursday        |                                   |                                   | Generic Lab. Sem-I<br>117 |                    |                       | Anal Elect-II Lab.<br>EH-III<br>117 | Anal Elect-III Lab.<br>EH-III<br>117 |                                  |                                  |
| Friday          | Embedded Lab. Bt-1<br>EH-V<br>117 | Embedded Lab. Bt-1<br>EH-V<br>117 |                           |                    |                       |                                     |                                      |                                  |                                  |
| Saturday        |                                   |                                   | VAC - Sem I<br>104        | VAC - Sem I<br>104 |                       |                                     |                                      |                                  |                                  |

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Prindpal

Comptroller / Time Table

Principal